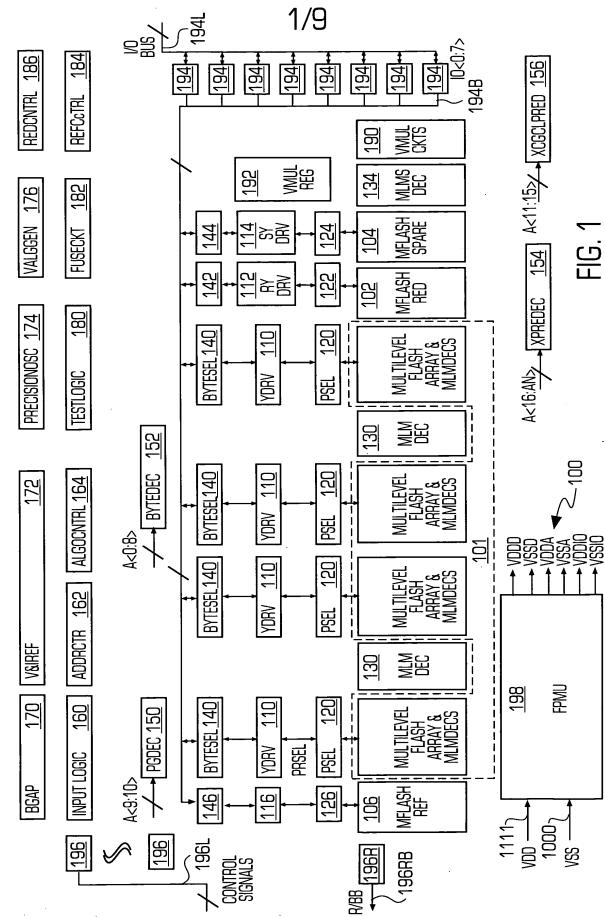
"Replacement Sheet"
Utility Patent Application for:
UNIFIED MULTILEVEL CELL MEMORY

INVENTORS: HIEU VAN TRAN ET ALJ Filed: September 9, 2003 // Docket No.: 351913-992830





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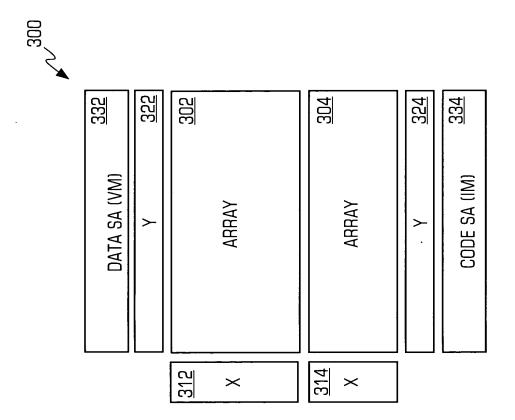
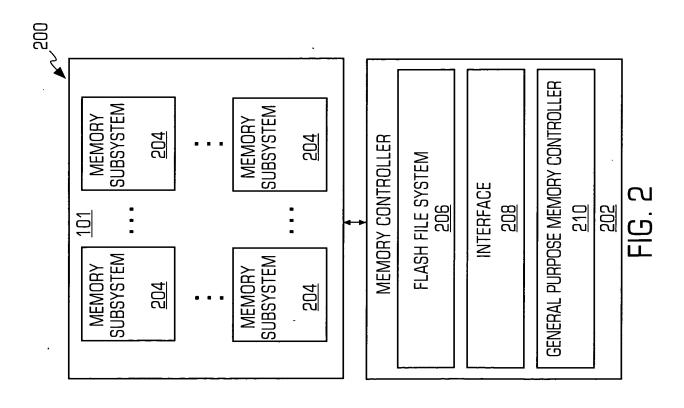
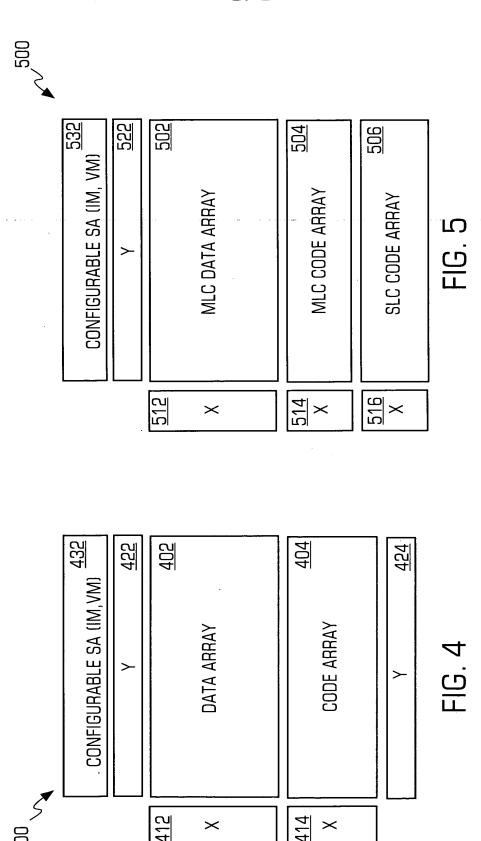


FIG. 3



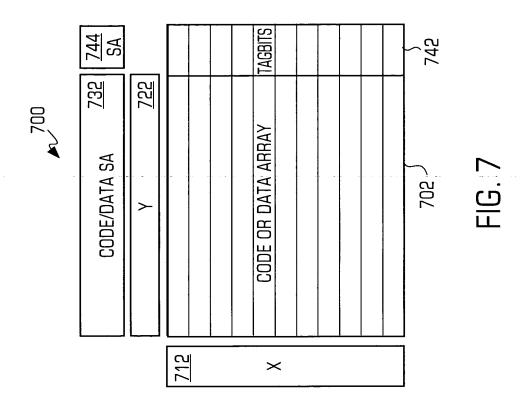
Filed: September 9, 2003 // Docket No.: 351913-992830

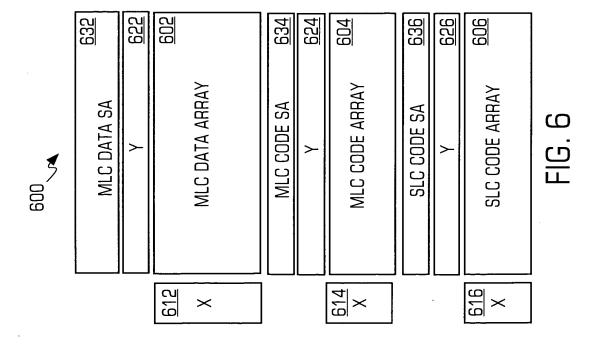
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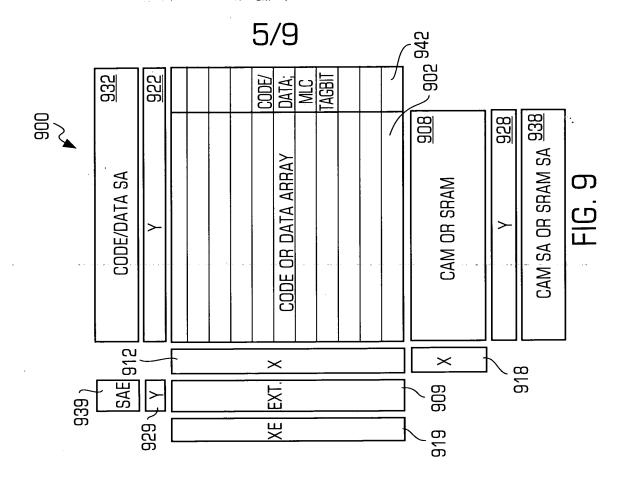
Filed: September 9, 2003 // Docket No.: 351913-992830

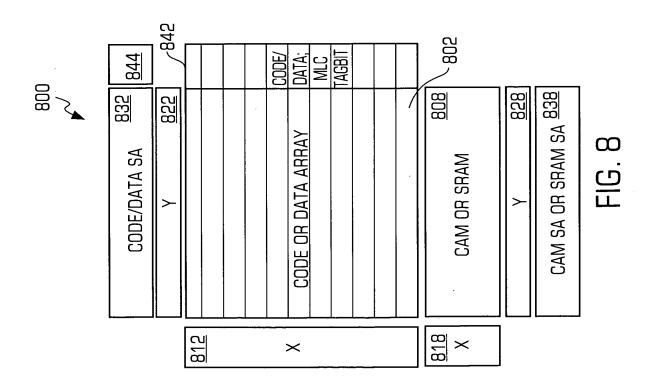
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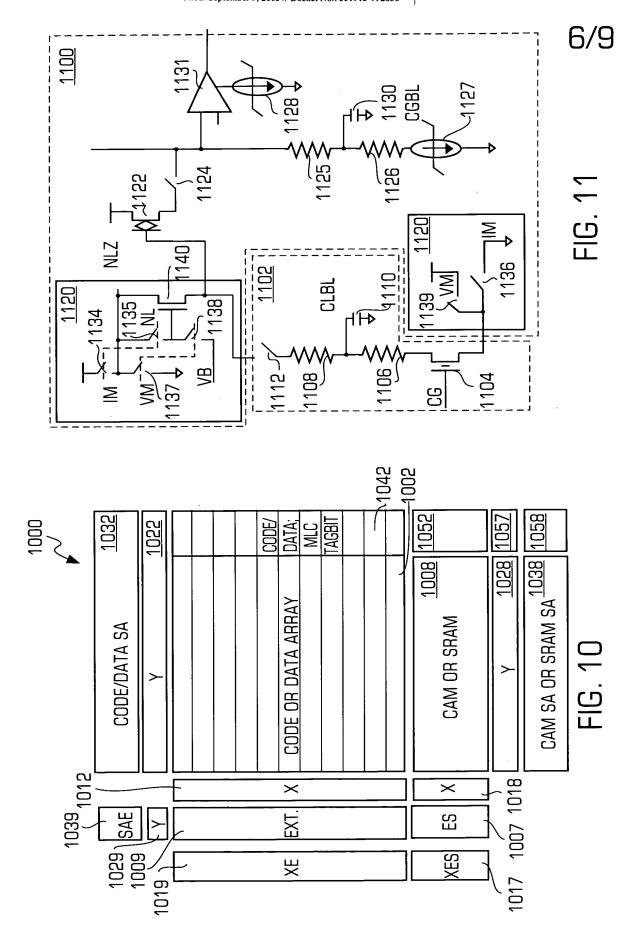


INVENTORS: HIEU VAN TRAN ET AL.



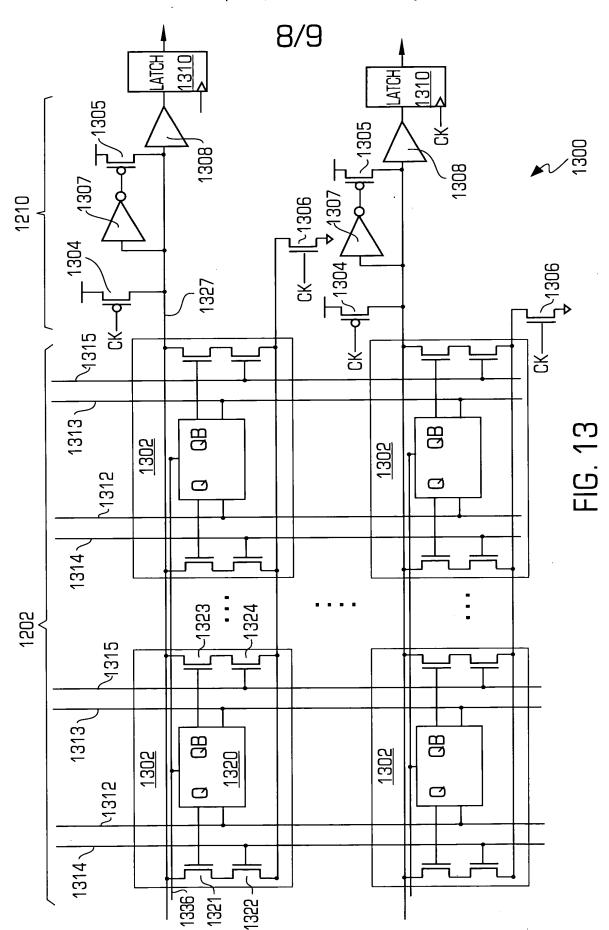


INVENTORS: HIEU VAN TRAN ET AL.



INVENTORS: HIEU VAN TRAN ET AL. Filed: September 9, 2003 // Docket No.: 351913-992830 FIG. 12 7/9 \sim 1212 1200 ADDRESS TRANSLATION ARRAY SA'S, Latches, Buffers 111 1210 PIPE-LINE STAGE 1204 BIT LINE DRIVERS, COMPARE DATA DRIVERS & MASK LINE DRIVERS SENSE AMPS, LATCHES, BUFFERS MATCH LINE ARRAY 1202 MATCH LINE 1208 ~ WORD LINE DRIVERS

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INVENTORS: HIEU VAN TRAN ET AL.

